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PATENT

U.S. UTILITY Patent Application

<p>O.I.P.E.</p> <p>For <i>AK</i></p> <p>SCANNED <i>83</i> O.A. <i>MM</i></p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/930021	CONT/PRIOR D	CLASS 711 712	SUBCLASS 1	ART UNIT 2185 182 2182	EXAMINER Lee
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APPLICANTS

Monte Mar
Warren Snyder

TITLE

Programming methodology and architecture for an analog programmable system on a chip

PTO-2040
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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg	Figs. Drwg	Print Fig	Total Claims	Print Claim for O.G.
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